



Mixed-Signal Excellence

Mixel announces two new PLLs featuring higher performance, reduced power, cost and die area at the 2007 FSA Expo and Conference

The high performance Spread Spectrum solution offers lower integration cost at smaller die area, and the high precision PLL offers higher output frequency granularity, while maintaining 50% duty cycle at lower power for even and odd output divisors.

September 12, 2007 San Jose, CA – Mixel Inc. today announced availability of the MXL-PLL-SS, a Spread-Spectrum Phased Locked Loop (SSPLL) solution and MXL-PLL-SYN-HP, a High Precision PLL (HPPLL). The SSPLL is designed to provide greater design flexibility by partitioning the SSPLL into two highly independent blocks: the PLL block and the spread-spectrum block. This allows the SoC designer to feed a high frequency clock, available from an existing PLL on the SoC, directly to the SS block, without the need for another area consuming PLL. The SS IP's modular solution offers smaller die area, lower power consumption, and higher performance. The HPPLL offers higher output frequency divider granularity and near-ideal duty cycle at lower power consumption.

“Our goal is to provide our customers with higher performance PLLs, while reducing their SoC die size, cost, and EMI. In many of today's SoC's, a high frequency clock is already available. In such cases, our SS technology enables the SoC designer to generate a spread-spectrum version of that clock, without the need for the large loop filter that consumes most of the area of a traditional SS-PLL implementation. Additionally, this modularity simplifies implementation of standards where spread-spectrum is an optional feature, such as PCI-E and SATA.” said Ashraf Takla, Mixel President and CEO. “The HPPLL offers superior output frequency granularity and 50% duty cycle for both even and odd output divisors at lower power consumption. These are clear differentiating factors that Mixel brings to its customers and partners today”, he added.

Mixel will be using this SSPLL technology to offer spread-spectrum option in its PCI-Express and SATA IPs. The MXL-PLL-SS and MXL-PLL-SYN-HP are offered in 0.13um and 0.18um process technologies and soon will be offered in smaller feature sizes.

About Mixel

Mixel is a leading provider of silicon-proven mixed-signal IP cores. Mixel provides its customers and partners with outstanding mixed-signal IP cores, creating in the process a differentiating technology that helps set their products apart. Mixel's mixed-signal IP portfolio includes SerDes (compatible with PCI Express, SATA, XAUI, Fibre Channel), Transceivers (LVDS, MIPI, MDDI, DDR2, PCI-X, SSTL, HSTS, CE-ATA, CardBus, Parallel ATA), PLL, DLL, ADC, DAC, Low-voltage detectors and low-voltage BGR references. For more information please go to www.mixel.com or call Mixel marketing at 408-942-9300 X 140.

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