Mixel Announces the Availability of its 4.25 Gbps High Performance, Highly Programmable, SerDes IP's Characterization report

The MXL4254A SerDes is a four-channel SerDes suitable for PCI Express, SATA, Fibre channel, Sonet, XAUI and other serial interface standards.

San Jose, CA, April 17, 2007 – Mixel Inc. today announced the availability of its MXL-SRDS-4254's extensive characterization report. The MXL-SRDS-4254 is a high performance quad SerDes IP with the highest programmability available on the market. It is targeted for a broad range of serial interface standards. This high performance, low power IP has four channels, each running at 1 to 4.25 Gbps.The IP is silicon-proven in TSMC and Chartered 130nm process technologies. The extensive characterization report is now available.

"The MXL-SRDS-4254 is by far the most programmable SerDes available on the market" said Ashraf Takla, Mixel President and CEO. "The highly programmable features of our 2nd generation SerDes technology, together with the exceptionally low jitter performance, high input jitter tolerance, and low power dissipation, make the MXL-SRDS-4254 a unique solution for SOC applications. We have invested in the resources needed to extensively characterize our most advanced IPs as we sharpen our focus on commercializing Mixel's technology. The result is a full characterization report that shows superior performance and high yield" he added.

Some of the unique features of the MXL-SRDS-4254 are the programmable output voltage swing, programmable termination resistors at the transmitter and receiver, programmable transmitter pre-emphasis, programmable receiver post-equalization, programmable frequency multiplication factor, and wide data-rate range. The De-Serializer can tolerate a large number of transition-free UI, and have high tolerance to frequency mismatch. The modular design offers great flexibility for different channel configurations and the robust design facilities porting to other process nodes and foundries.

The IP is silicon-proven in TSMC and Chartered 130nm process technologies. The IP has been extensively characterized to further validate its production worthiness. A full set of characterization data is now available for review.

About Mixel

Mixel Inc. is a leading provider of silicon-proven mixed-signal IP cores. Mixel provides its customers and partners with outstanding mixed-signal IP cores, creating in the process a differentiating technology that helps set their products apart. Mixel's mixed-signal IP portfolio includes SerDes (compatible with PCI-Express, SATA, RapidIO, XAUI, and Fibre Channel), Transceivers (LVDS, MIPI, MDDI, DDR2, PCI-X, SSTL, HSTS, CE-ATA, CardBus, and Parallel ATA), PLL, DLL, ADC, DAC, Low-voltage detectors and low-voltage BGR. More information can be found at www.mixel.com .

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