Features:

- Complies with MIPI Standard for M-PHY, Draft Specification v0.95.
- Dual-simplex point-to-point interface with ultra low voltage differential signaling
- Slew-rate control for EMI reduction
- Supports all HS modes (GEAR 1-3)
- Supports all Type-I LS modes (GEAR 0-7)
- Supports Type-II LS mode
- 1-6Gbps data rate in HS mode
- 0.01-576Mbps data rate in LS mode
- Suitable for copper and optical media
- Modular design to allow for all possible configurations
- Low power dissipation

General Description:

The MXL-M-PHY-MIPI is a high-frequency low-power, low-cost, Physical Layer IP compliant with the MIPI Alliance Standard for M-PHY. The IP can be used as a physical layer for many applications, including interfaces for display, camera, audio, video, power management and Baseband to RFIC.

It is compatible with DigRF v4, Unipro 1.5 and will support the upcoming Universal Flash Storage (UFS) standard.

By using efficient BURST mode operation with scalable speeds, significant power savings can be obtained.

Selection of signal slew rate and amplitude allows reduction of EMI/RFI, while maintaining low bit error rates.

Block Diagrams:

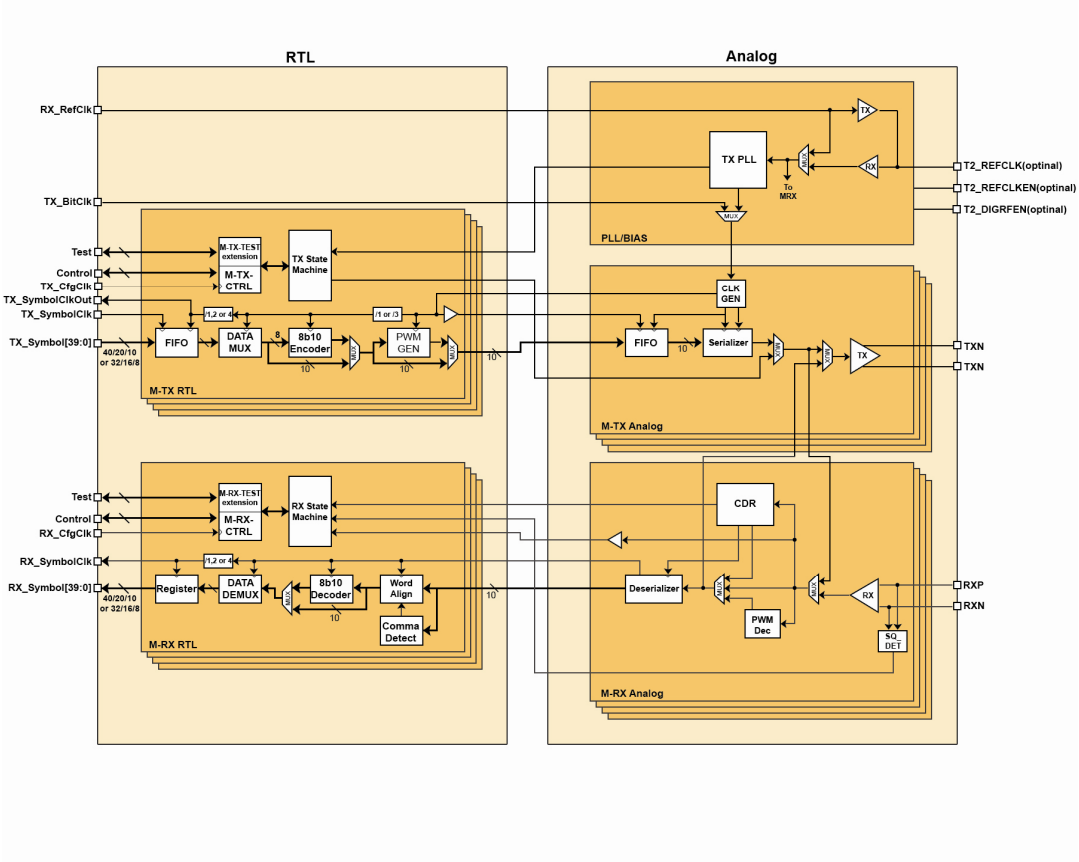

Figure 1 - Block Diagram of fully featured M-PHY

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